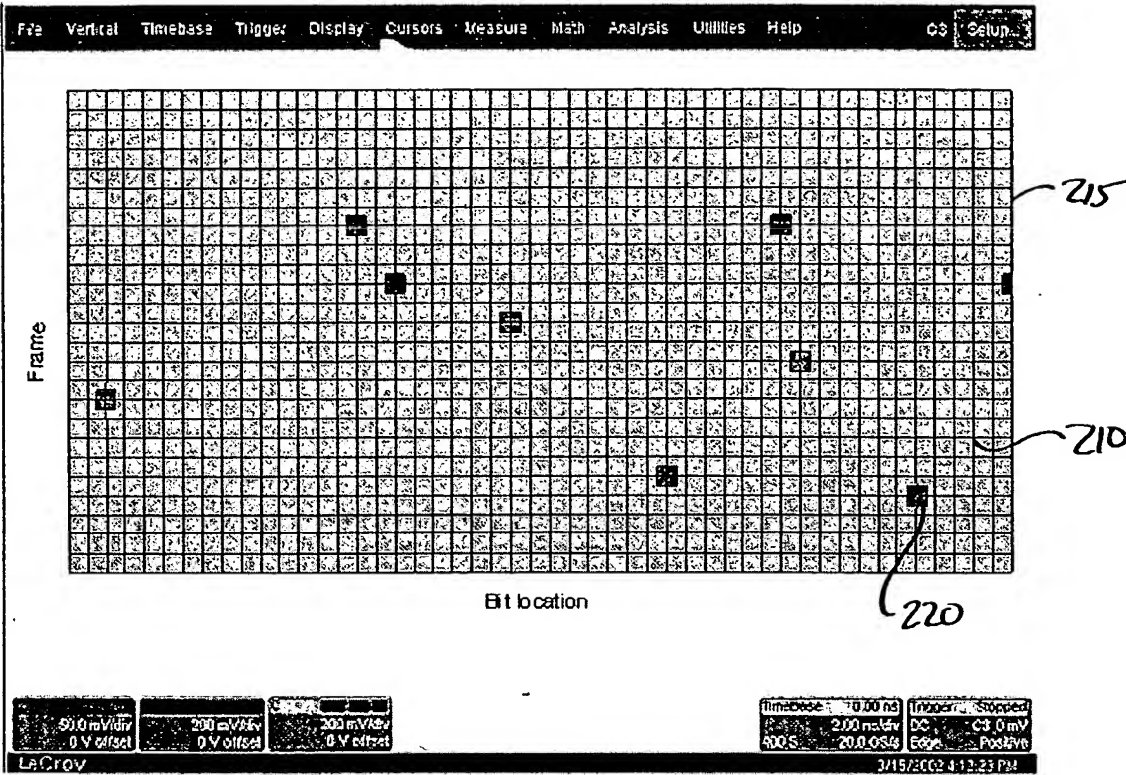


Figure 1

PATENT
455610-2580

205



Actual bit error map:

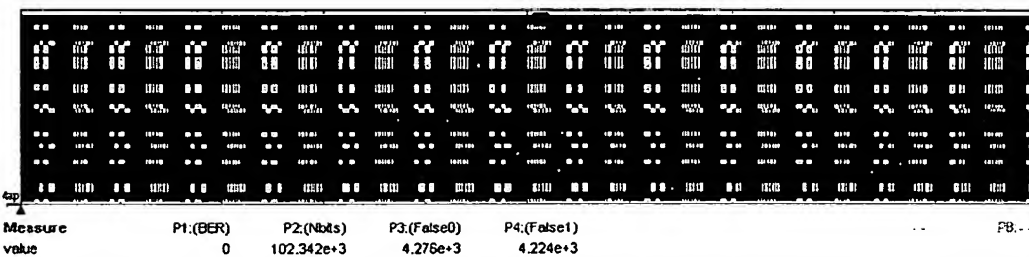


FIGURE 2

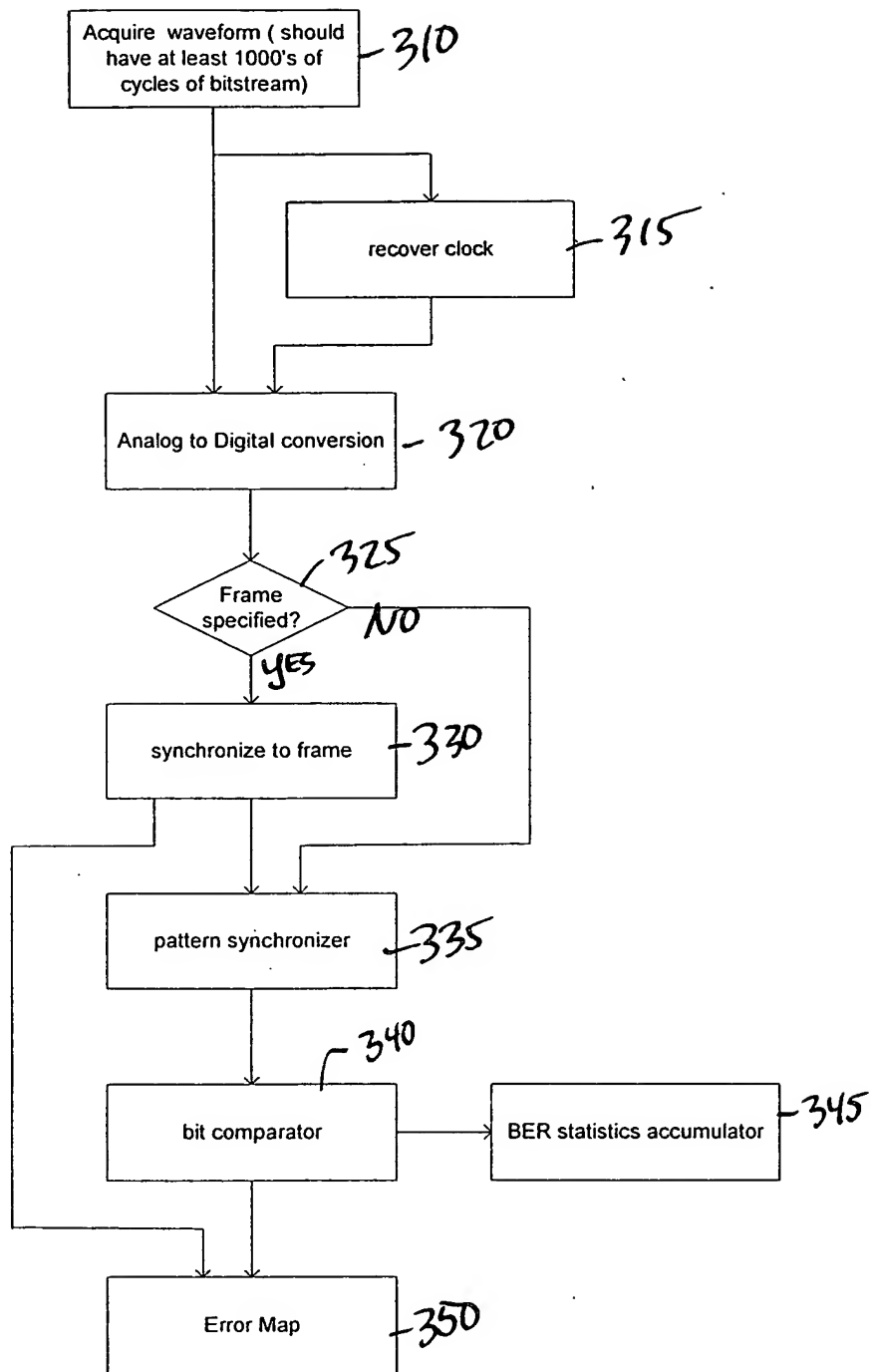


Figure 3

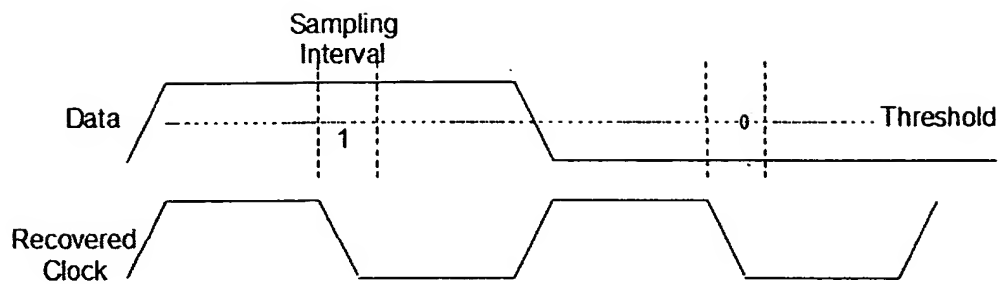


Figure 4

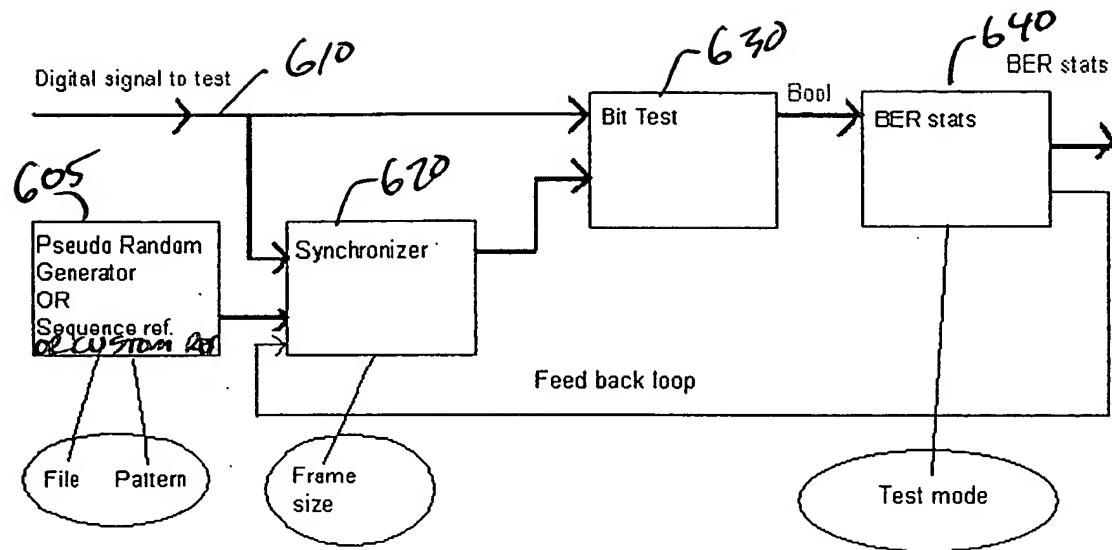
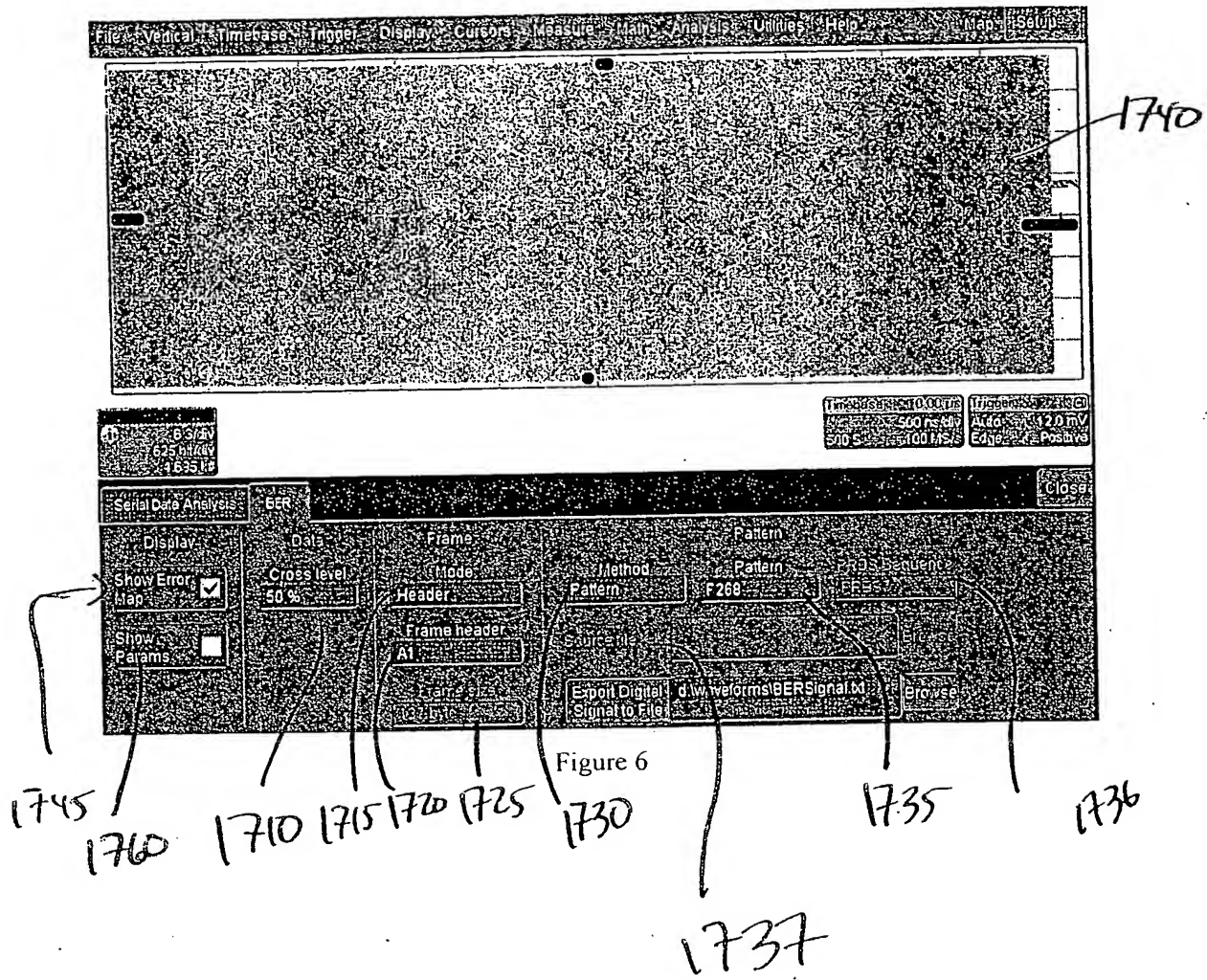


Figure 5



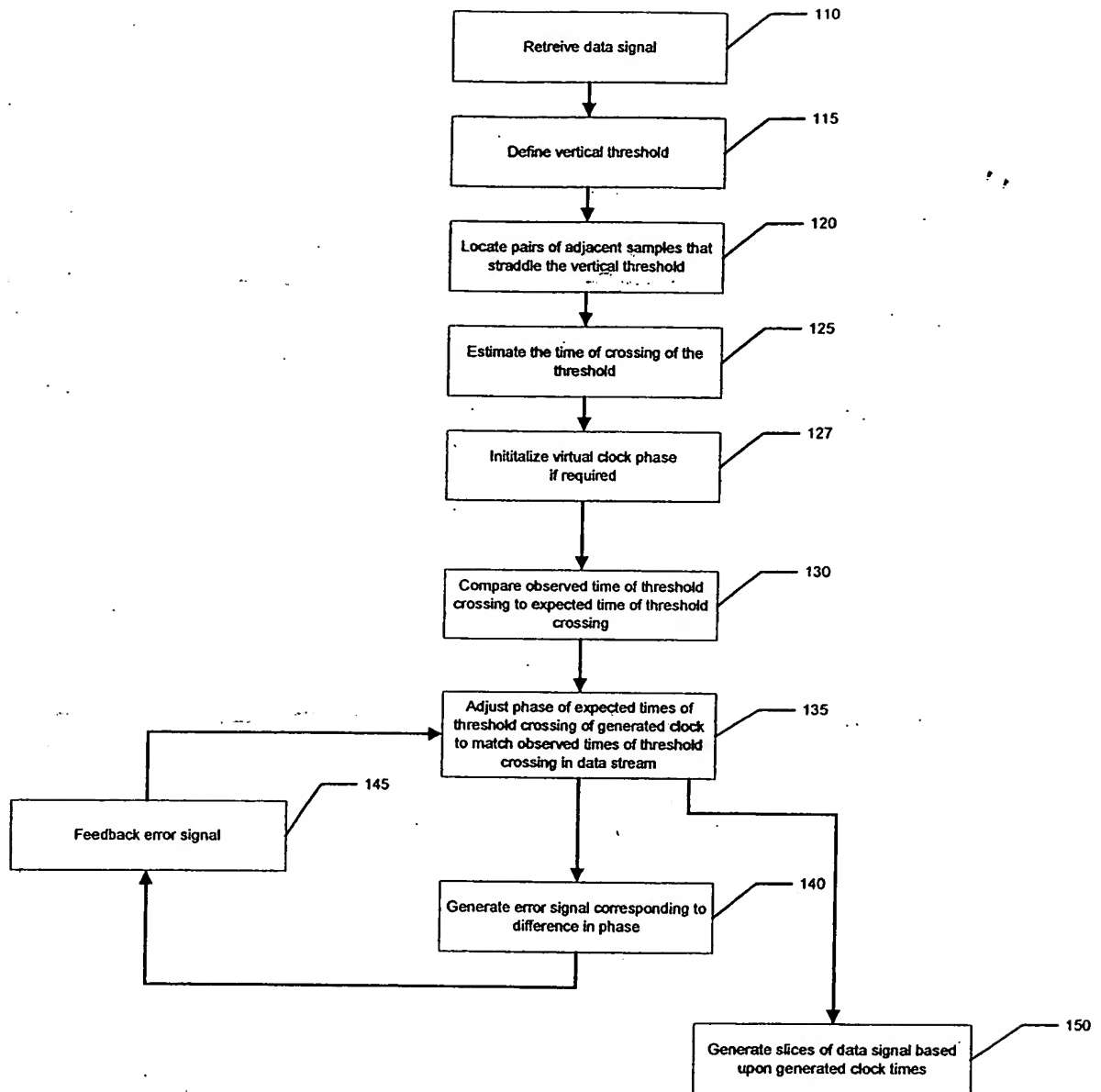


Figure 7

